

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of	)	Group Art Unit: Unknown
	)	
Douglas Robert Farrenkopf	)	Examiner: Unknown
	)	
Application No. New	)	INFORMATION DISCLOSURE
	)	<u>STATEMENT</u>
Filed: Herewith	)	
	)	
For: ESD PROTECTION CIRCUIT FOR HIGH-	)	
VOLTAGE, HIGH DV/DT PADS	)	
	)	
	)	
	)	

Mail Stop Patent Application  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Applicant submits herewith information of which applicant is aware, which applicant believes may be material to the examination of this application and in respect of which there may be a duty to disclose in accordance with 37 CFR § 1.56.

This Information Disclosure Statement:

- (a) ☒ accompanies the new patent application submitted herewith. 37 CFR § 1.97(a).
- (b) ☐ is filed within three months after the filing date of the application or within three months after the date of entry of the national stage of a PCT application as set forth in 37 CFR § 1.491.
- (c) ☐ as far as is known to the undersigned, is filed before the mailing date of a first Office Action on the merits.

(d) ☐ is filed after the first office action and more than three months after the application's filing date or PCT national stage date of entry filing but, as far as is known to the undersigned, prior to the mailing date of either a final rejection or a notice of allowance, whichever occurs first, and is accompanied by either the fee (\$180) set forth in 37 CFR § 1.17(p) or a certification as specified in 37 CFR § 1.97(e), as checked below.

(e) ☐ is filed after the mailing date of either a final rejection or a notice of allowance, whichever occurred first, and is accompanied by the fee (\$180) set forth in 37 CFR § 1.17(i)(1) and a certification as specified in 37 CFR § 1.97(e), as checked below. This document is to be considered as a petition requesting consideration of the information disclosure statement.

[If either of boxes (d) or (e) are checked above, the following "certification" under 37 CFR § 1.97(e) may need to be completed.] The undersigned certifies that:

(f) ☐ Each item of information contained in the information disclosure statement was cited in a communication mailed from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this information disclosure statement.

(g) ☐ No item of information contained in this information disclosure statement was cited in a communication mailed from a foreign patent office in a counterpart foreign application or, to the knowledge of the undersigned after making reasonable inquiry, was known to any individual designated in 37 CFR § 1.56(c) more than three months prior to the filing of this information disclosure statement.

A list of the patent(s) or publications(s) is set forth on the attached Form PTO-1449 (Modified). A copy of the items on PTO-1449 (Modified) is supplied herewith:

(h) ☒ each                      (i) ☐ none                      (j) ☐ only those listed below:

Those patent(s) or publication(s) which are marked with an asterisk (\*) in the attached form PTO-1449 (Modified) are not supplied because they were previously cited by or submitted to the Office in a prior application no. \_\_\_\_\_ filed \_\_\_\_\_ and relied upon in this application for an earlier filing date under 35 U.S.C. § 120.

A concise explanation of relevance of the described above and attached information is:

(k) ☒ not given

(l) ☐ given for each listed item

(m) ☐ given for only non-English language listed item(s) [Required]

(n) ☐ is in the form of an English language copy of a Search Report from a foreign patent office, issued in a counterpart application, which refers to the relevant portions of the references [copy attached].

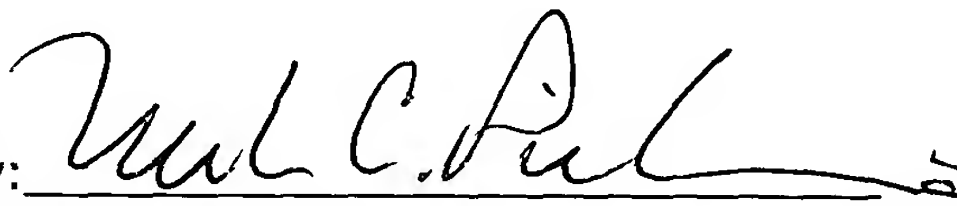
While the information disclosed in this Information Disclosure Statement may be "material" pursuant to 37 CFR § 1.56, it is not intended to constitute an admission that the

information referred to is "prior art" for this invention unless specifically designated as such.

In accordance with 37 CFR § 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 CFR § 1.56(a) exists. It is submitted that the Information Disclosure Statement is in compliance with 37 CFR § 1.98 and MPEP § 609 and the Examiner is respectfully requested to consider the above-described and attached information.

Respectfully submitted,

Dated: 9-15-03

By:   
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Atty. Docket No: 100-23000 (P05670)

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FORM PTO-1449  INFORMATION DISCLOSURE CITATION IN AN APPLICATION  Express Mail Number <u>EV342470480US</u> (Use several sheets if necessary)	Docket Number (Optional) 100-23000 (P05670)	Application No. New
	Applicant(s) Douglas Robert Farrenkopf	
	Filing Date Herewith	Group Art Unit Unknown

U.S. PATENT DOCUMENTS												
EXAMINER INITIAL	DOCUMENT NUMBER							DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	6	5	5	2	8	8	6	04/22/03	Wu et al.	361	56	06/29/00
	5	5	2	1	7	8	9	05/28/96	Ohannes et al.	361	111	03/15/94
	5	3	0	1	0	8	4	08/05/94	Miller	361	91	08/21/91
	5	2	3	9	4	4	0	08/24/93	Merrill	361	91	08/14/92
	4	8	2	9	3	5	0	05/09/89	Miller	357	23.13	05/05/88

FOREIGN PATENT DOCUMENTS													
	DOCUMENT NUMBER							DATE	COUNTRY	CLASS	SUBCLASS	Translati n	
												Yes	No

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)												
	Albert Z.H. Wang, "On-Chip ESD Protection For Integrated Circuits", An IC Design Perspective, 2002, pps. 46-47.											
EXAMINER	DATE CONSIDERED											

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP §609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.